

MALLA REDDY ENGINEERING COLLEGE (AUTONOMOUS)

(An UGC Autonomous Institution, Affiliated to JNTUH, Hyderabad Accredited 2nd time by NAAC with 'A' Grade & NBA and Recipient of World Bank Assistance under TEQIP--II S.C. 1.1)

Maisammaguda (H), Medchal-Malkajgiri District, Secunderabad,

Telangana State — 500100

www.mrec.ac.in

Department of Electrical and Electronics Engineering

CIRCULAR

Date: 20.10.2019

All the students are hereby informed that Value Added Course on NI LABVIEW CLAD COURSE from Nov 04th to Nov 08th 2019 is being organized by the Electrical and Electronics Engineering. The resource person for the course is Mr.B.Anil Kumar Project engineer ...Students are advised to register their names to the programme coordinator Mr.P.Ganesh on or before 27.10.2019 and utilize this opportunity to enhance their skills by attending the programme.

The detailed schedule of the programme will be displayed in the notice board.

Head of the Department

Copy to:

1) Circulation in Students classroom

2) All HOD's

3) Notice Boards

4) PA to principal for filing.

Walla Reddy Engineering College (Autonomous)

Maisammaguda, Dhulapally,

About the Institution

Malla Reddy Engineering College (Autonomous) is one of the reputed engineering colleges in Hyderabad, Telangana. MREC (A) is part of Malla Reddy Group of Institutions (MRGI), founded by Honourable Sri. Ch. Malla Reddy, M.P., Govt. of Telangana State. The college situated is in serene, lush green en viron mentin Maisammaguda. GundlapochampallyMedchal(M),Medchal-Malkajgiri District Telangana-500100.

The college was established in 2002 and is an autonomous institution approved by UGC and affiliated to JNTUH. The college is reaccredited by NAAC with 'A' Grade (II Cycle) and was conferred autonomous status by JNTUH in 2011 and by UGC in 2014 for a period of 6 years. Our eligible UG and PG programs received NBA accreditation and some of them received reaccreditation too. The college caters to wide ranging aspirations and goals of student communities by offering new courses in UG courses provides PG courses and MBA along with programs in various streams of Engineering & Technology and Management. It boasts of world-class infrastructure and wellequipped laboratories in all departments and is skillfully and smartly guided by Dr. S. Sudhakara Reddy, Principal, MREC (A) who have a rich teaching and industrial experience.

Advisory Committee

Chief Patrons: Sri. Ch. Malla Reddy, Minister-

Telangana State-India. Founder Chairman Malla Reddy Group of

Institutions Patrons:

Sri.Ch. Mahender Reddy

Secretary, MRGI Dr.Ch.Bhadra Reddy President, MRGI

Co-Patrons: Dr. A Raveendra Principal, MREC (A)

Convener: Dr. N. Rajeswaran HOD EEE

Coordinator: Mr.P.Ganesh

Assistant Professor, EEE

Resource Person: Mr.B.Anil Kumar Project engineer CADD Technologies Pvt Ltd Telangana.

Organizing Committee:

Dr.D.Raja Reddy, Assoc.Prof, EEE Dr.A.V.Sudhakara Reddy, Assoc. Prof, EEE Mr.P. Kamalakar, Assoc. Professor, EEE Mr. T. Sanjeeva Rao, Assoc. Professor, EEE Mr.Ch NarendraKumar, Assoc. Professor. EEE Mr.V.Ganesh Kumar, Assoc. Professor, EEE Mr. D.Ramesh, Asst.Prof, EEE

Mr.KSS Naga Teja, Asst. Prof. EEE Mrs. Y. Sudha, Asst. Prof. EEE Mr. P. Ganesh, Asst. Prof. EEE Mrs.K.Chetaswi, Asst.Prof. EEE

Mr. Madhu Mohan Reddy A, Asst. Prof. EEE Mrs. V. Suma Deepthi, Asst. Prof. EEE

Mrs. Pooja Patil, Asst. Prof. EEE





A Five-Day Value added Course On

"NI LABVIEW CLAD COURSE" (Nov 04th to Nov 08th 2019)



Organized by Department of Electrical and Electronics Engineering MALLA REDDY ENGINEERING COLLEGE

(AUTONOMOUS) MAIN CAMPUS

An UGC Autonomous Institution, Approved by AICTE & Affiliated to JNTUH-Hyderabad Reaccreditedby NAACwith'A'Grade (II Cycle) Maisammaguda(H), Gundlapochampally (V), Medchal (M), Medchal - Malkajgiri District Telangana - 500100, India.

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About the Department

The Department of Electrical and Electronics Engineering has been established since the inception of the institution in the year 2002. The Department has good infrastructure facilities and is equipped with full-fledged laboratories to fulfill the curriculum needs. The Department has well experienced faculty. Around one-third of the faculty members in the department are Doctorate. The department has good number of sanctioned projects, funded by different agencies/industries. The Department is intended to be allotted a Research Centre by JNTU Hyderabad.

Overview of the Program

The primary responsibility of faculty is not only to inspire students towards a higher vision but also create a strong sense of bonding between the institution and the students to nurture a stress-free holistic environment. To enhance the quality of life for the student members enabling them to introspect and learn techniques that imbibe ethics & morals and help prepare students for active and successful participation in a modern society, producing individuals of high character, probity, and honor.

Proposed course helps for an integral aspect for the design of photovoltaic systems in every country in the world. It allows you to perform the entire process of designing a PV system, from the preliminary assessment of energy generation to the production of final system documentation.

Objectives of the Program

- To use the techniques, skills and modern engineering tools necessary for engineering.
- To function on multi-disciplinary teams in the area of power systems, power electronics.
- Understanding the design of equipment using MATLAB easily.
- Ability to communicate effectively on the software tools.

Topics to be covered

- ❖ LABVIEW Design modules
- ❖ State machine architecture, Dialogue and Interface
- Electrical components Compressive testing
- * Hybrid Vehicle Design

Certificate

After successful completion of the course the certificates shall be issued to the participants.

Outcome of the program

After completing the Course, Design can be done on system components using LABVIEW software. Able to calculate Event structure, Notifies and Queues calculation and Use of Flat sequence structure.



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Maisammaguda (H), Medchal-Malkajgiri District, Telangana State – 500100

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING Value Added Course

"NI LABVIEW CLAD COURSE" 04-11-2019 to 08-11-2019 Programme Schedule

| Date | Time | Topic | Resource Person | |
|------------|------------------|--|------------------|--|
| | 10:10 – 11.00AM | Inauguration | | |
| - 8 | 11:00 -11:15AM | Tea Break | | |
| 04-11-2019 | 11.15 – 12:45 PM | LabVIEW Introduction | | |
| 04-11-2019 | 12:45 – 1:30PM | Lunch | Mr.Vijay chander | |
| | 1:30 – 2:30 PM | Update mode of chart | | |
| 6 | 2:30 – 2:45PM | Tea Break | | |
| | 2:45 -4:00 PM | Example and Exercise | | |
| | 9:30 – 11.00AM | Flat sequence Structure | | |
| | 11:00 -11:15AM | Tea Break | Mr.Vijay chander | |
| | 11.15– 12:45 PM | Example and Exercise | | |
| 05-11-2019 | 12:45 – 1:30PM | Lunch | | |
| | 1:30 – 2:30 PM | Variable(Local and Global) and Race Condition | | |
| | 2:30 – 2:45PM | Tea Break | | |
| | 2:45 -4:00 PM | Example and Exercise | | |
| | 9:30 – 11.00AM | Property node | | |
| | 11:00 -11:15AM | Tea Break | | |
| | 11.15 – 12:45 PM | Example and Exercise | | |

| 06-11-2019 | 40.45.4.00005 | Lunch | |
|------------|------------------|-----------------------------|---------------------|
| 00-11-2017 | 12:45 – 1:30PM | Lunch | Mr.Vijay chander |
| | 1:30 – 2:30 PM | Dialogue and User Interface | wir. v ijay chander |
| | 2:30 – 2:45PM | Tea Break | |
| - | 2:45 -4:00 PM | Example and Exercise | |
| | 9:30 – 11.00AM | State Machine Architecture | |
| | 11:00 -11:15AM | Tea Break | |
| 07-11-2019 | 11.15 – 12:45 PM | Example and Exercise | |
| | 12:45 – 1:30PM | Lunch | |
| | 1:30 – 2:30 PM | File I/O | Mr.Vijay chander |
| | 2:30 – 2:45PM | Tea Break | |
| | 2:45 -4:00 PM | Example and Exercise | |
| | 9:30 – 11.00AM | Event structure | |
| 08-11-2019 | 11:00 -11:15AM | Tea Break | |
| | 11.15 – 12:45 PM | Notifier and Queues | |
| | 12:45 – 1:30PM | Lunch | Mr.Vijay chander |
| | 1:30 – 2:30 PM | Example and Exercise | |
| | 2:30 – 2:45PM | Tea Break | |
| | 2:45 -4:00 PM | Valedictory | 7 |

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Maisammaguda (H), Medchal-Malkajgiri District, Telangana State – 500100

Department: EEE

Course: B.Tech

Date: 4/11/2019 to 8/11/2019

DEPARTMENT OF ELECTRICAL and ELECTRONICS ENGINEERING

List of Students Enrolled for Value Added Course Academic Year 2019-2020

Name of the Value Added Course: NI LABVIEW CLAD COURSE

| Sl.No. | Roll No. | Name of the Student | Year/Semester |
|--------|------------|------------------------|---------------|
| 1 | 16J41A0201 | AEDLA YASHASWI | IV/I |
| 2 | 16J41A0203 | BELLAMKONDA SRINIVAS | IV/I |
| 3 | 16J41A0204 | BHUKYA AKHILA | IV/I |
| 4 | 16J41A0205 | BIJJA SANDEEPKUMAR | IV/I |
| 5 | 16J41A0208 | BODDU SRUJAN VAMSHI | IV/I |
| 6 | 16J41A0211 | BUDIGE KARTHIK | IV/I |
| 7 | 16J41A0213 | C SAISREENADH | IV/I |
| 8 | 16J41A0215 | CHITTIMALLA HARI PRIYA | IV/I |
| 9 | 16J41A0218 | DOSADA MANOJ REDDY | IV/I |
| 10 | 16J41A0219 | G SANJANA | IV/I |
| 11 | 16J41A0222 | GUGULOTH RAVI CHANDRA | IV/I |
| 12 | 16J41A0224 | IRSHAD ALAM KHAN | IV/I |
| 13 | 16J41A0225 | K.BHAGYA LAKSHMI | IV/I |
| 14 | 16J41A0228 | KOBBA DIVYA | IV/I |
| 15 | 16J41A0232 | KOTTAKONDA SAIRAM | IV/I |
| 16 | 16J41A0233 | KUKKALA UDAY | IV/I |
| 17 | 16J41A0236 | M VISHAL | IV/I |
| 18 | 16J41A0240 | MD ATIQ | IV/I (P) |
| 19 | 16J41A0242 | MOHAMMAD SHAREEF | IV/I |
| 20 | 16J41A0243 | MOTHE SRUJAN DEV | IV/I |
| 21 | 16J41A0244 | MULKALA DRAVYA | IV/I |
| 22 | 16J41A0246 | PATI VINEETH | IV/I |
| 23 | 16J41A0247 | PENDAM BHARATH | IV/I |
| 24 | 16J41A0250 | SAINDLA SAI KIRAN | IV/I |
| 25 | 16J41A0252 | SHAIK ANEEF | IV/I |
| 26 | 16J41A0256 | THALLA ARUN | IV/I |
| 27 | 16J41A0255 | TAGARAM VEERESH | IV/I |

Maisammaguda, Dhulapally, ost Via Kompally), Sec'bad-500 100.



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|-----|------------|-------------------------|-------|
| 28 | 16J41A0256 | THALLA ARUN | IV/I |
| 29 | 16J41A0257 | THAMMINA PALLAVI | IV/I |
| 30 | 16J41A0259 | VAKADANI TRIVENI | IV/I |
| 31 | 17J45A0202 | K SAHARIKA | III/I |
| 32 | 17J45A0204 | NIMMAKAYALA SUJITH | III/I |
| 33 | 17J45A0206 | R.S.V. SAI SATHWIK | III/I |
| 34 | 17J45A0207 | BHASKARA NAVYA | III/I |
| 35 | 17J45A0208 | CHELKALA NIKHITHA | III/I |
| 36 | 17J41A0201 | AKULA NAGARAJU | III/I |
| 37 | 17J41A0205 | B MRINALINE | III/I |
| 38 | 17J41A0203 | ANKAM PRUTHVIRAJ | III/I |
| 39 | 17J41A0210 | BHUKYA SANDHYA | III/I |
| 40 | 17J41A0211 | BOJJA SADHANA | III/I |
| 41 | 17J41A0213 | BURUGU VAMSI | III/I |
| 42 | 17J41A0218 | G VINAY KUMAR | III/I |
| 43 | 17J41A0221 | GUNJI PRAVALIKA | III/I |
| 44 | 17J41A0223 | KAMMARI RAGHAVI | III/I |
| 45 | 17J41A0224 | KAMPATI SPANDANA | III/I |
| 46 | 17J41A0227 | KETHIRI SAI KRISHNA | III/I |
| 47 | 17J41A0230 | K BALAJI RUTHVIK | III/I |
| 48 | 17J41A0231 | K DAYANANDAM | III/I |
| 49 | 17J41A0232 | M VISHNUPRIYA | III/I |
| 50 | 17J41A0234 | M SINDHUJA | III/I |
| 51 | 17J41A0237 | MOHAMMED SHOEB AKTHER | III/I |
| 52 | 17J41A0239 | MUNUKUTLA SAI SRI VAMSI | III/I |
| 53 | 17J41A0240 | NADIPELLI RAHUL RAO | III/I |
| 54 | 17J41A0241 | NAGULA VENU | III/I |
| 55 | 17J41A0242 | NANDEPU DEVSISH | III/I |

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DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Date: 08.11.2019

Report of Value added Course on NI LABVIEW CLAD Course

A five days value added Course on NI LABVIEW CLAD COURSE was organized by the department of Electrical and Electronics Engineering and the event was coordinated by Mr. P. Ganesh, Asst. Professor, EEE Department from 04th Nov. to 08th Nov. 2019 and 55 students are attended from III and IV years. The resource person of the training programme was Mr. B. Anil kumar, Project Engineer, CADD Technologies Pvt. Ltd, Telangana. The participants are trained in the field of LABVIEW and its engineering applications. It gave the conceptual level of knowledge to industry standards. This course is further focused on,

- The use of techniques, skills and modern engineering tools necessary for simulation of electrical engineering applications.
- LABVIEW design modules.
- Real Time implementation of developed control algorithms in various system applications.
- Electrical components compressive testing.
- Hybrid Vehicle design.

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DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING <u>Certificate of Completion</u>

This is to certify that Mrs./Ms./Mr. BELLAMKONDA SRINIVASbearing Roll Number 16J41A0203has successfully completed Value added Course in "NI LABVIEW CLAD Course" Organized by the Department of Electrical and Electronics Engineering from 04-11-2019 to 08-11-2019

Coordinator

Malla Reddy Engrom Dh. (Autorouda, Dh. Maisammaguda, Ser

HOD-FFF

Principal









(Autonomous)

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DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

Certificate of Completion

This is to certify that Mrs./Ms./Mr. BHUKYA AKHILAbearing Roll Number 16J41A0204has successfully completed Value added Course in "NI LABVIEW CLAD Course" Organized by the Department of Electrical and Electronics Engineering from 04-11-2019 to 08-11-2019

Malla Reddy Engineering College

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Principal









(Autonomous)

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DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

Certificate of Completion

This is to certify that Mrs./Ms./Mr. BIJJA SANDEEPKUMARbearing Roll Number 16J41A0205has successfully completed Value added Course in "NI LABVIEW CLAD Course" Organized by the Department of Electrical and Electronics Engineering from 04-11-2019 to 08-11-2019

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DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING <u>Certificate of Completion</u>

This is to certify that Mrs./Ms./Mr. BODDU SRUJAN VAMSHIbearing Roll Number 16J41A0208has successfully completed Value added Course in "NI LABVIEW CLAD Course" Organized by the Department of Electrical and Electronics Engineering from 04-11-2019 to 08-11-2019

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DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

Certificate of Completion

This is to certify that Mrs./Ms./Mr. BUDIGE KARTHIK bearing Roll Number 16J41A0211has successfully completed Value added Course in "NI LABVIEW CLAD Course" Organized by the Department of Electrical and Electronics Engineering from 04-11-2019 to 08-11-2019

Malla Reddy Engineering College

(Autonomous)

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Raund Principal